What is claimed is:

1. A flat panel display device comprising:

a lower electrode area formed on a substrate and defining luminescent pixels;

at least one auxiliary pattern formed on one at least edge area of the substrate so as to be separated from the lower electrode area; and

an insulating layer formed on an area excluding the luminescent pixels so as to be overlapped in part with the at lest one auxiliary pattern.

2. A flat panel display device comprising:

a plurality of lower electrodes extending on a substrate in one direction;

a plurality of luminescent pixels formed on and defined by the lower electrodes;

a plurality of connection tabs formed on one edge of the substrate in a direction crossing with the extending direction of the lower electrodes so as not to be overlapped with the lower electrodes;

an insulating layer formed on an area excluding the luminescent pixels and the lower electrodes;

at least one auxiliary pattern formed at an edge area of the substrate so as to be overlapped at least in part with insulating layer to prevent the insulating layer from peeling; and

an upper electrode formed over the lower electrodes so as to be contacted with the connection tabs.

3. The device of claim 1, further comprising an organic luminescent layer on the respective luminescent pixels to emit light.

4. The device of claim 2, wherein adhesiveness between the insulating layer and auxiliary pattern is superior to that between the insulating layer and substrate.

5. The device of claim 2, wherein portions of the respective auxiliary patterns have areas overlapped with the insulating layer and wherein the at least one auxiliary pattern is formed at least one corner of at least a most outer one of the connection tabs in the extending direction of the lower electrodes.

6. The device of claim 2, wherein, in case that the insulating layer is formed to expose portions of the

lower electrodes and the connection tabs only for a connection to a driving circuit of the device, the auxiliary pattern is formed at the edge area of the substrate so as to be overlapped with at lest a portion of the insulating layer formed at a side not to be connected to the driving circuit.

7. The device of claim 2, wherein the insulating layer is formed to be overlapped with upper edges of the connection tabs.

8. The device of claim 2, wherein the auxiliary pattern is formed of a same material of forming the connection tabs.

9. The device of claim 2, wherein an interval between the adjacent lower electrodes is narrower than that between the connection tab and the lower electrode.

10. The device of claim 2, further comprising at least a second auxiliary pattern between the lower electrodes and connection tabs, respectively, so as to be overlapped in part with the insulating layer.

11. The device of claim 10, wherein the insulating layer has a via part so as to be overlapped with edges of the second auxiliary pattern and wherein the via part is contacted with the upper electrode.

12. The device of claim 2, further comprising partition walls formed on the insulating layer between columns of the luminescent pixels in the extending direction of the lower electrodes so as to electrically isolate the upper electrode every column unit of the luminescent pixels.

13. The device of claim 2, wherein the auxiliary pattern is formed of a transparent conductive material.

14. The device of claim 13, wherein the transparent conductive material is ITO(indium tin oxide).

15. The device of claim 2, further comprising an auxiliary electrode on a predetermined area of the respective lower electrodes and connection tabs, the auxiliary electrode having a resistance lower than that of the lower electrode and that of the connection tab.

16. The device of claim 2, wherein the insulating layer is formed one of oxide, nitride, inorganic matter dissolved in a solvent for spin-coating, photoresist, polyimide, and polyolefin.

17. A method of fabricating a flat panel display device, comprising the steps of:

forming a plurality of lower electrodes on a substrate so as to extend in one direction wherein luminescent pixels are defined over the lower electrodes;

forming a plurality of connection tabs on one edge of the substrate in a direction crossing with the extending direction of the lower electrodes so as not to be overlapped with the lower electrodes and forming at least one auxiliary pattern on an edge area of the substrate;

forming an insulating layer on an area excluding the luminescent pixels and the lower electrodes so as to be overlapped in part with the at least one auxiliary pattern; and

forming an upper electrode over the lower electrodes so as to be contacted with the connection tabs.

18. The method of claim 17, further comprising a step of forming an organic luminescent layer on the respective luminescent pixels.

19. The method of claim 17, further comprising a step of forming partition walls on the insulating layer between columns of the luminescent pixels in the extending direction of the lower electrodes so as to electrically isolate the upper electrode every column unit of the luminescent pixels.

20. The method of claim 17, further comprising a step of forming an auxiliary electrode on a predetermined area of the respective lower electrodes and connection tabs wherein the auxiliary electrode has a resistance lower than that of the lower electrode and that of connection tab.